

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: NISHIHARA et al.

Serial No.: Rule 53(b) of 09/380,735

Filed: November 26, 2003

For: METHOD OF FABRICATING SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE

Art Unit: Unassigned (2811 previously in parent application)

Examiner: Unassigned (D. Kang previously in parent application)

PRELIMINARY AMENDMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

November 26, 2003

Sir:

Please amend the above-identified application, prior to examination thereof,
as listed in the following, and as set forth on the following pages:

Amendments to the Specification; and

Remarks are included following the amendments.